

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	188	YOSHIDA-DAISUKE.in.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2005/09/12 07:48
L2	561656	(CANON or KABUSHIKI). as.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2005/09/12 07:49
L3	3	("6670938").PN.	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2005/09/12 07:49
L4	4	((("6670938") or ("20040075633"))).PN.	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2005/09/12 10:28
L5	14326	345/30,33,48,50,53,54,55,76, 79,87,88,89,90,91,92,93,94, 95,96,98,99,100,204,205,206, 208,209,210.ccls.	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:28
L6	1982	315/169.3.ccls.	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:28

L7	4902	349/33,34,36,37,38,39,41,42,43,46,47,48,54.ccls.	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:28
L8	506621	(LCD or (liquid adj crystal))	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:28
L9	42091	(active adj matrix)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:28
L10	53673	((data or signal) and (scan\$4 or select\$3)) adj (line or electrode or wire or column or row))	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29
L11	224356	((column and row) or ((vertical and horizontal) adj (line or electrode or wire)))	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29
L12	301316	(pixel)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29
L13	2396712	(switch or transistor or TFT)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29

L14	448087	(intersect\$3)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29
L15	119189	((counter\$3 or oppos\$4 or common or insulat\$3 or (single adj crystal)) adj substrate)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29
L16	3854809	(driv\$3)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:29
L17	117	((("1st" or first) and ("2nd" or second)) adj (common adj (data or signal)) adj (line or electrode or wire or column or row))	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:30
L18	542119	(polar\$3)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:30
L19	7940937	(first and a second transfer switch)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:30
L20	995148	(column inversion)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:30

L21	21004	((("1st" or first) and ("2nd" or second)) adj frame)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:30
L22	5158	((odd and even) adj (line or electrode or wire or column or row))	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31
L23	1077	(dot adj inver\$4)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31
L24	144444	((p-channel or p-type) and (n-channel or n-type))	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31
L25	128023	CMOS	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31
L26	13926	((positive and negative) adj polarity)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31
L27	897	((high\$3 and low\$3 and (mid\$4 or cent\$3)) adj voltage)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31

L28	47941	(range near voltage)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31
L29	51924	((("0" or zero) or ("1" or one)) adj volt)	US-PGPU B; USPAT; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2005/09/12 10:31


9/12/05